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Docket No.: 12898-US-PA
Application No.: 10/709,430

To the Drawings:

FIGs. 5-7 are amended by adding --(PRIOR ART)--.

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REMARKS

Present Status of the Application

Applicant appreciates that claims 5, 6, 14, 15 and 24 are considered to be allowable.

However, claims 1, 2, 7, 8, 9, 10, 11, 16-20, 22, and 25-28 are rejected under 35 U.S.C. 102(b) as being anticipated by Hu et al. (U. S. Pub. 2003/0030687; hereinafter Hu). Claims 3, 12 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hu in view of Dodd (U. S. Pub. 2003/0063297). Claim 4, 13 and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hu in view of Li (U. S. Patent 6, 871,933). In addition, specification, drawings, and abstract are objected. Applicant has amended specification, drawings, abstract, and claims. Claims 1-28 remain pending in the present application, and reconsideration of those claims is respectfully requested.

Discussion of Claim Rejections under 35 USC 102

Claims 1, 2, 7, 8, 9, 10, 11, 16-20, 22, and 25-28 are rejected under 35 U.S.C. 102(b) as being anticipated by Hu. Applicant respectfully traverses the rejections for at least the reasons set forth below.

1. As for example shown in FIG 2A, FIG. 2B, and FIG. 3A, the present invention used at least one **data input** terminal (i.e. F1-F4) of the identification unit 202 to receive the content of the memory unit 230. The at least one control input terminals (i.e. A, B) receive the control signals to determine and output the content stored in the memory unit.

The data input terminal is the input terminal for identification unit to receive the

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information from a memory unit. The identification unit 202 reads the basic information stored in the memory unit via the data input terminal of the identification unit 202. The data input terminal is not used for outputting the determined content.

2. Regarding claims 1, 10, 22 and 25 being rejected by the Office Action, Fig. 7 and Fig. 11 of Hu discloses the identification circuit 75. The Office Action considers the common output end 89 as the data input terminal of the present invention. Further, the Office Action considers the identifying cell 84 as the memory unit of the present invention. Clearly, these considerations are improper. Applicant respectfully disagrees.

In Fig. 11 of Hu, each identifying cell 84 is a part of the identification circuit 75 to provide one-bit code to the printer ([0030], lines 6-10). Therefore, the identifying cell 84 disclosed in Hu is not the memory unit 230 of the present invention to be read by the identification unit disclosed in the present invention, as recited in independent claims 1, 10, and 25.

Further, clearly, the data input terminal (F1-F4) of the identification unit in the present invention is for receiving signal for the identification unit, but not for outputting signal from the identification unit. On the contrary, the common output end 89 of Hu is obviously an output line for outputting signal from the identification unit 79 and is transmitted to the output line 79 via the switch 92. Therefore Hu does not disclose the data input signal of the present invention. Hu teaches away the present invention.

3. Regarding currently rejected independent claims 16 and 25, the Office Action considers

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the *power supply line 82* as the control signal of the present invention, and the *identifying cell 84* as the memory unit of the present invention. Applicant respectfully disagrees.

In addition to the foregoing reasons applied to independent claims 1, 10, and 25, the further features as recited in amended claims 16 and 25 are further distinguish over the prior art.

In the present invention, the control signal is logically operated with the read content from the memory unit, so as to determine the actual output content.

It seems that Examiner analogizes applying control voltage VA disclosed in Hu as the arrangement of a signal level of the control signal in claim 16 and 25 of the present invention (lines 7-8 of page 5 in the Office Action). However, the control voltage VA disclosed in Hu is the voltage applied to the address line (lines 17-19, column 2 of Hu). It is applied to the heating circuit not to the identification circuit. *Furthermore, Hu does not teach or suggest any control signal that is logically operated with the read content from the memory unit to determine the actual output content.* In Hu, each power supply line 82 is directly input to each identifying cell 84 by the working voltage or identifying voltage lower than the working voltage, so as to produce the one-bit code. Indeed, Hu does not disclose the features of the present invention, as recited in claims 16 and 25.

For at least the foregoing reasons, independent claims 1, 10, 16, and 25 are distinguishable over the prior art, and therefore dependent claims 2, 7, 8, 9, 11, 17-20, 22, and 26-28 should be allowed as well.

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Discussion of Claim Rejections under 35 USC 103

Dependent claims 3, 12 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hu in view of Dodd. Dependent claim 4, 13 and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hu in view of Li. Applicant respectfully traverses the rejections for at least the reasons set forth below.

Dependent claims 3, 4, 12, 13, 21 and 23 are dependent claims from independent claims 1, 10, and 16. With at least the foregoing reasons applied to independent claims 1, 10, and 16, Hu failed to disclose the present invention and teaches away the present invention.

Dodd is cited in combination with Hu for rejecting claims 3, 12 and 21. However, Dodd does not disclose the missing features in Hu, with respect independent claims 1, 10, and 16. With at least the same reasons, claims 3, 12 and 21 are distinguishable over the prior art.

Li is cited in combination with Hu for rejecting claims 4, 13 and 23. However, Dodd does not disclose the missing features in Hu, with respect independent claims 1, 10, and 16. With at least the same reasons, claims 4, 13 and 23 are distinguishable over the prior art.

For at least the foregoing reasons, Applicant respectfully submits that independent claims 1, 10, 16 and 25 patently define over the prior art references, and should be allowed. For at least the same reasons, dependent claims 2-9, 11-15, 17-25, and 26-28 patently define over the prior art references as well. Claims 5, 6, 14, 15 and 24 have been considered to be allowable.

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CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1-28 of the invention patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,

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